## Oscillatory Recurrent Gated Neural Integrator Circuits (ORGaNICs)

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Heeger PNAS (2017) Heeger & Mackey arXiv (2018)

### Working memory: cognitive psychology

Cognitive process that is responsible for temporarily **maintaining** and **manipulating** information.

Example from language:

Problem of long-term dependencies

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Example from language:

Part I: Working memory

The athlete realized his goals, which were formed during childhood, to qualify for this year's Olympic team,  $\ldots$ 

Problem of long-term dependencies

## Working memory: cognitive psychology

Cognitive process that is responsible for temporarily **maintaining** and **manipulating** information.

Example from language:

The athlete realized his goals, which were formed during childhood, to qualify for this year's Olympic team,  $\dots$  (quickly/were unattainable).

Problem of long-term dependencies

## <image>







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Part III: Canonical computation: sensory &

motor processing

















## Processing delays mean the brain has to make predictions:







### Events unfold over time

































### Implications for neuroscience

- Working memory/executive functions, motor preparation/ control, and sensory processing may share a common computational foundation.
- 2) Working memory > short-term memory.
- 3) Complex dynamics:
  - Unified model for sustained delay-period activity, sequential activity, and complex dynamics.
  - Read out in spite of complex dynamics.
- 4) Experiments:
  - Example of testable prediction: thalamic input changes the effective time constant and recurrent gain of a PFC neuron.
  - New conceptual framework / new paradigm: gated integration, reset, effective time constant.

### Implications for AI

- 1) Go complex: simple harmonic motion is everywhere!
- 2) Stability:
  - Avoid exploding gradients by rescaling recurrent weight matrix after each gradient update (s.t. largest eigenvalue = 1).
  - Avoid vanishing gradients by using rectification instead of saturating nonlinearities.
- 3) Reset & update gates = gated integration, reset, effective time-constant.
- 4) Warp time by scaling the intrinsic time constants.
- 5) Neuromorphic (analog VLSI) implementation.











